



GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY

Sector 16-C, Dwarka, New Delhi – 110078

EXAMINATION DIVISION

Conduct Branch-I

PROPOSED PRACTICAL DATE SHEET FOR END TERM EXAMINATIONS (NOVEMBER 2015)

Programme: M. Tech. (CSE, IT and VLSI Design)

Only for C-DAC Students {Regular & Reappear}

Date/Day	Course/Semester	Course Code	Time: 9:30 AM onwards
16.11.2015 Monday	CSE/ 1st Sem.	MECS-651	Advanced Data Structures Lab
	IT /1st Sem.	MEIT-651	Advanced Data Structures Lab
	VLSI /1st Sem.	MEVS-651	Digital System Design using Verilog Lab
17.11.2015 Tuesday	CSE/ 1st Sem.	MECS-653	Advance Software Engineering Lab
	IT /1st Sem.	MEIT-653	Advance Software Engineering Lab
	VLSI /1st Sem.	MEVS-653	Advanced VLSI Design Lab
18.11.2015 Wednesday	CSE/ 1st Sem.	MECS-655	Advances in Data & Computer Communications Lab
	IT/ 1st Sem.	MEIT-659	Advances in Data & Computer Communications Lab
	VLSI /1st Sem.	MEVS-655	Algorithm Analysis and Design Lab
23.11.2015 Monday	CSE/ 3rd Sem.	MECS-751	Advanced Data Warehousing & Data Mining
	IT/ 3rd Sem.	MEIT-751	Advanced Data Warehousing & Data Mining
	VLSI /3rd Sem.	MEVS-751	VLSI Design Test and Testability Lab
24.11.2015 Tuesday	CSE/ 3rd Sem.	MECS-753	Software Testing
	IT/ 3rd Sem.	MEIT-759	Information Theory & Coding
	VLSI/ 3rd Sem.	MEVS-753	Analog Mixed Signal Design + Designing with FPPGA
26.11.2015 Thursday	CSE/ 3rd Sem.	MECS-757	Minor Project
	IT/ 3rd Sem.	MEIT-757	Minor Project
	VLSI/ 3rd Sem.	MEVS-757	Minor Project

Note: Any representation regarding the proposed dates/ paper etc. may be forwarded to the Controller of Examination / Officer Incharge (Conduct) through the Coordinator-cum-Convener of M. Tech. (Engineering Physics). program latest by 23rd October, 2015.

For any clarification and query please contact:

Section Officer (Conduct-I)	011-25302263
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(A.D. Lamba)
Dy. Registrar (Examinations)

(Prof. Pravin Chandra)
Controller of Examinations(Operations)