



Date Sheet/ April 2015

# GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY

Sector 16-C, Dwarka, New Delhi - 110078

## EXAMINATION DIVISION

### **Conduct Branch-I**

PROPOSED PRACTICAL DATE SHEET FOR END TERM EXAMINATIONS (MAY 2015)

Programme: M. Tech. (CSE, IT and VLSI Design) – Regular & Reappear

Only for C-DAC Students

Timing 10:00 AM Onwards

Date/Day	Course	Course Code	Subject
09.05.2015 Saturday	CSE	MECS-652	Object Oriented Software Engineering Lab
	IT	MEIT-652	Advanced Mobile Computing
	VLSI Design	MEVS-652	ESD
11.05.2015 Monday	CSE	MECS-654	Advanced Data Base Management System Lab
	IT	MEIT-654	Advanced Software Project Management
	VLSI Design	MEVS-654	Analog VLSI Design
13.05.2015 Wednesday	CSE	MECS-656	Advanced Algorithm Analysis & Design Lab
	IT	MEIT-656	Computer Graphics & Animation
	VLSI Design	MEVS-656	Low Power VLSI Design

**Note:** Any representation regarding the proposed dates/ paper etc. may be forwarded to the Controller of Examination / Officer Incharge (Conduct) through the Coordinator-cum-Convener of M. Tech. (CSE/IT and VLSI Design) program latest by 28<sup>th</sup> April, 2015.

**For any clarification and query please contact:**

Section Officer (Conduct-I)	011-25302263
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(A.D. Lamba)  
Dy. Registrar (Examinations)

(Prof. Pravin Chandra)  
Controller of Examinations(Operations)

